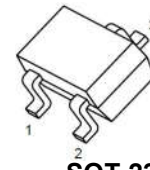


30V N -Channel MOSFET

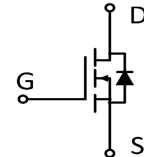
Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
30V	22m Ω @10V	5.8A
	23m Ω @4.5V	

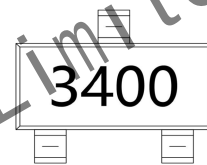


1:GATE
2:SOURCE
3:DRAIN

SOT-23-3L



Circuit diagram



R0 = Device code

Marking (Top View)

Features

- ◆ Very Low On-resistance $R_{DS(ON)}$
- ◆ Low C_{rss}
- ◆ Fast switching
- ◆ Improved dv/dt capability

Applications

- ◆ PWM Application
- ◆ Load Switch
- ◆ Power Management

Order information

Device	Package	Shipping
AN3400BAL0	SOT-23-3L	3000/Tape&Reel

Absolute maximum ratings

Parameter		Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	± 12	V
Drain Current ¹	$T_C = 25^\circ\text{C}$	I_D	5.8	A
	$T_C = 100^\circ\text{C}$		3.8	A
Drain Current - Pulsed		I_{DM}	23	A
Power Dissipation	$T_A = 25^\circ\text{C}$	P_D	1.36	W
Thermal Resistance, Junction-to-Ambient		$R_{\theta JA}$	92	$^\circ\text{C/W}$
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	$^\circ\text{C}$
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		TL	300	$^\circ\text{C}$

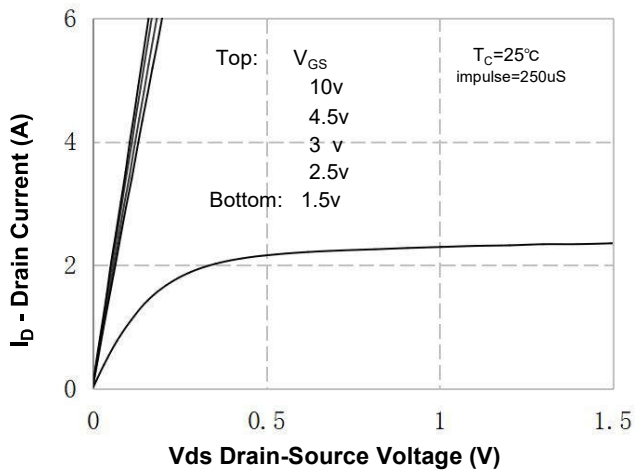
Electrical characteristics (T_A = 25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain - Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250μA	30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V			1	μA
Gate - Body Leakage Current	I _{GSS}	V _{GS} = ±12V, V _{DS} = 0V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	0.6	0.9	1.4	V
Drain-source On-resistance ²	R _{DS(on)}	V _{GS} = 10V, I _D = 5.8A		22	28	mΩ
		V _{GS} = 4.5V, I _D = 5A		23	30	
		V _{GS} = 2.5V, I _D = 4A		30	45	
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} = 15V, V _{GS} = 0V, f = 1.0 MHz		760		pF
Output Capacitance	C _{oss}			56		
Reverse Transfer Capacitance	C _{rss}			49		
Switching Characteristics						
Total Gate Charge	Q _g	V _{DS} = 15 V, I _D =5.6A, V _{GS} = 4.5V		8.6		nC
Gate-source Charge	Q _{gs}			1.1		
Gate-drain Charge	Q _{gd}			2.8		
Turn-on Delay Time	t _{d(on)}	V _{GS} =10V, V _{DS} =15V, R _G = 3 Ω ,R _L = 2.6 Ω		13		ns
Turn-on Rise Time	t _r			52		
Turn-off Delay Time	t _{d(off)}			25		
Turn-off Fall Time	t _f			10		
Source - Drain Diode Characteristics						
Drain to Source Diode Forward Voltage, V _{GS} = 0V, I _{SD} =5.8A, T _J = 25 °C	V _{SD}			0.85	1.2	V

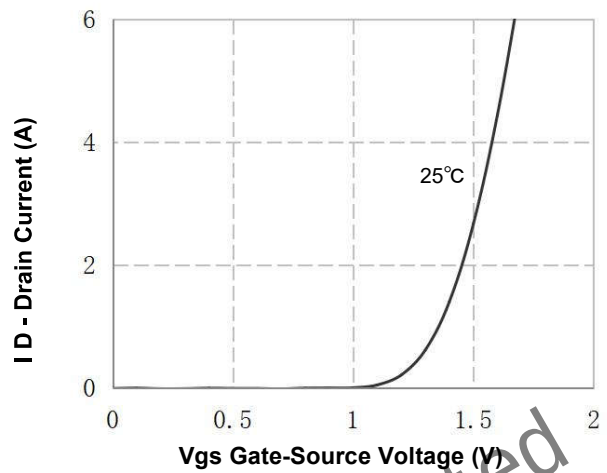
Notes:

1. Device mounted on FR-4 PCB, 1inch x 0.85inch x 0.062 inch
2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

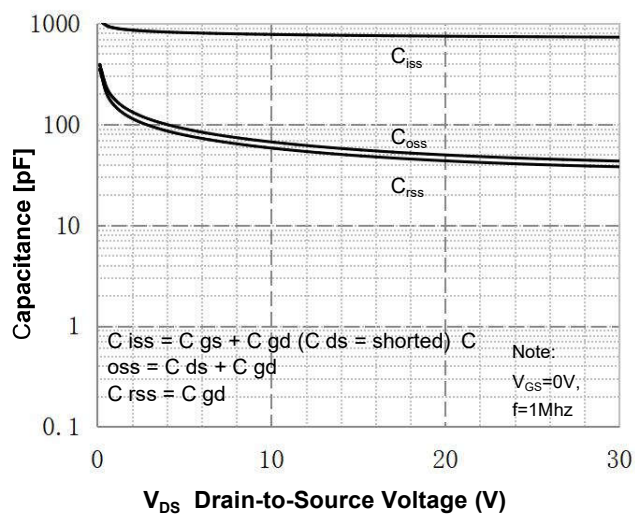
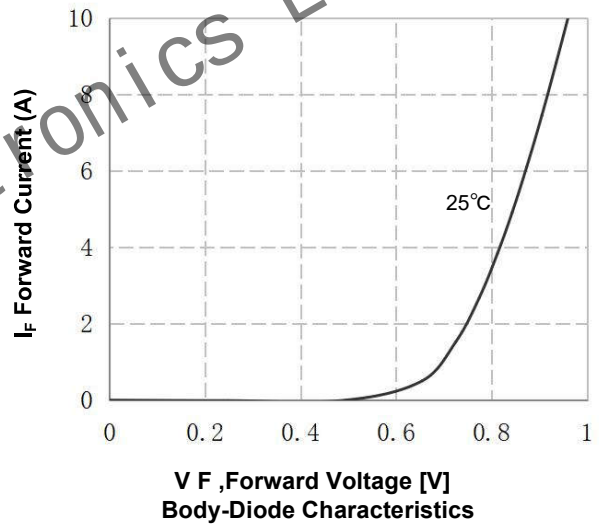
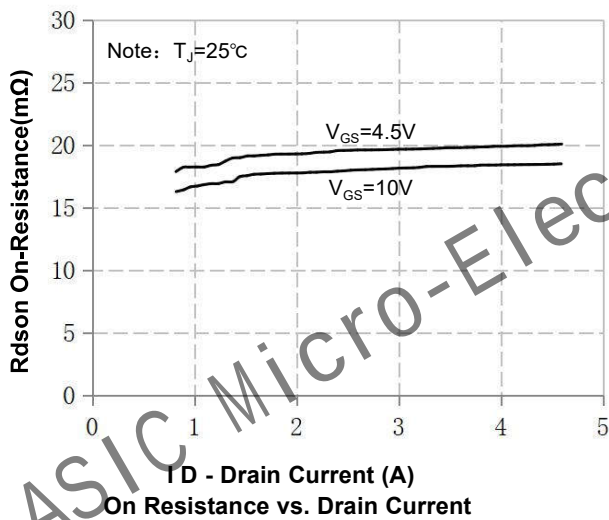
Typical characteristics ($T_A = 25^\circ\text{C}$, unless otherwise noted)



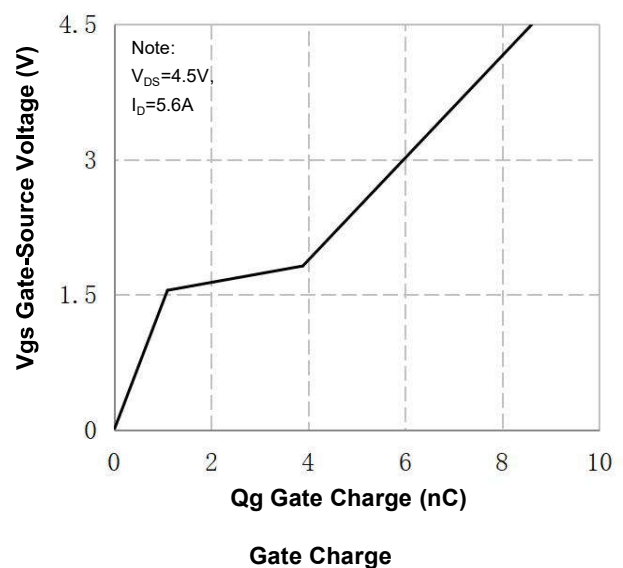
Output Characteristics



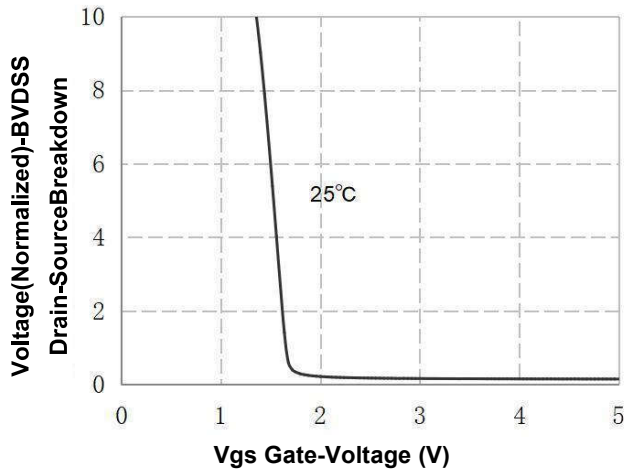
Transfer Characteristics



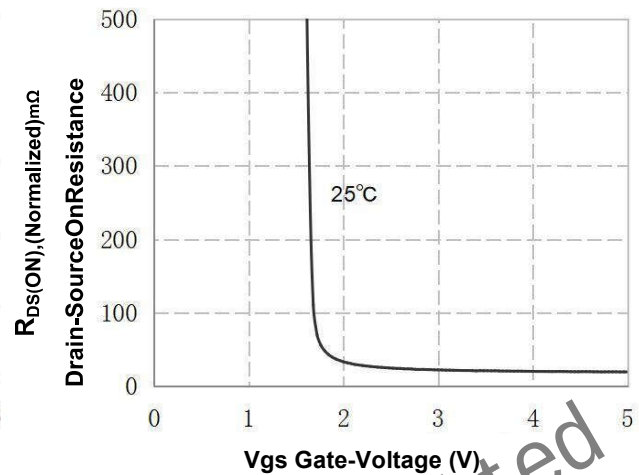
Capacitance Characteristics



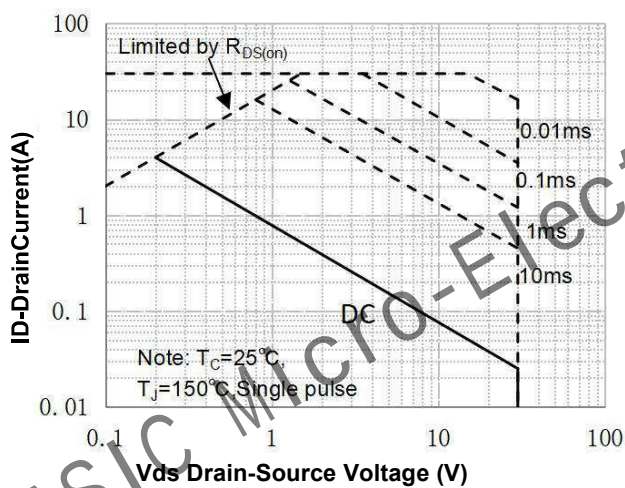
Typical characteristics ($T_A = 25^\circ\text{C}$, unless otherwise noted)



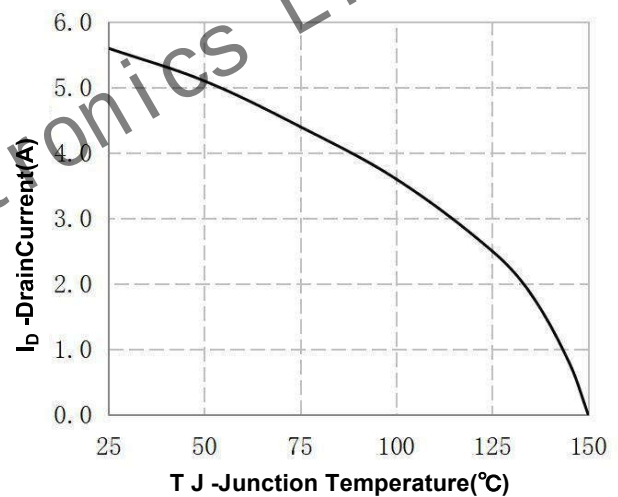
Breakdown Voltage Variation vs Gate-Voltage



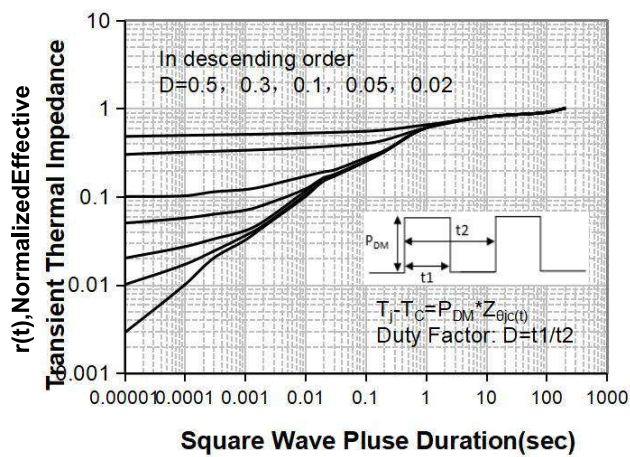
On Resistance vs. Gate-Source Bias



Maximum Safe Operating Area



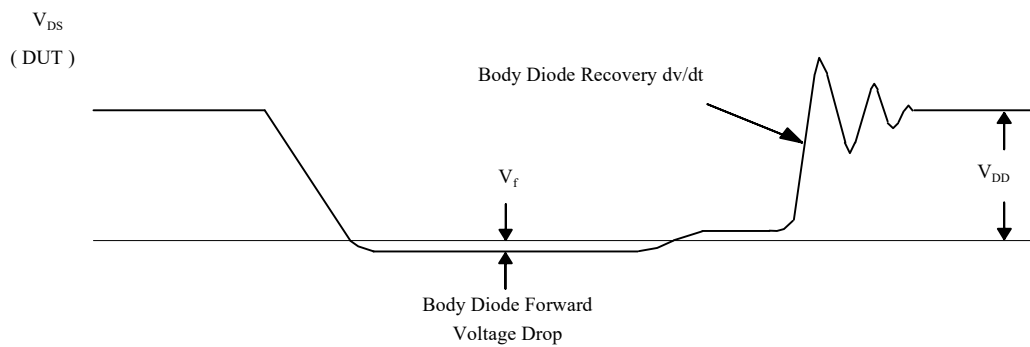
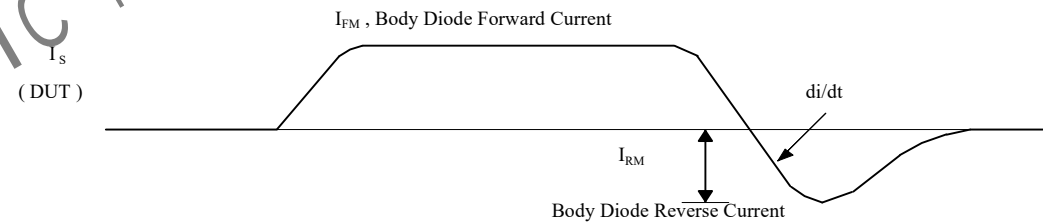
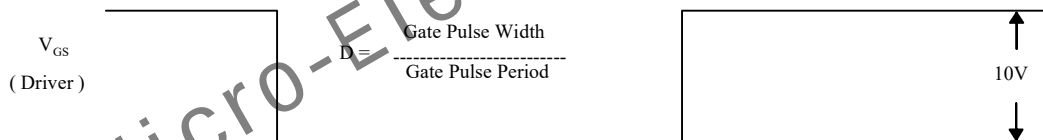
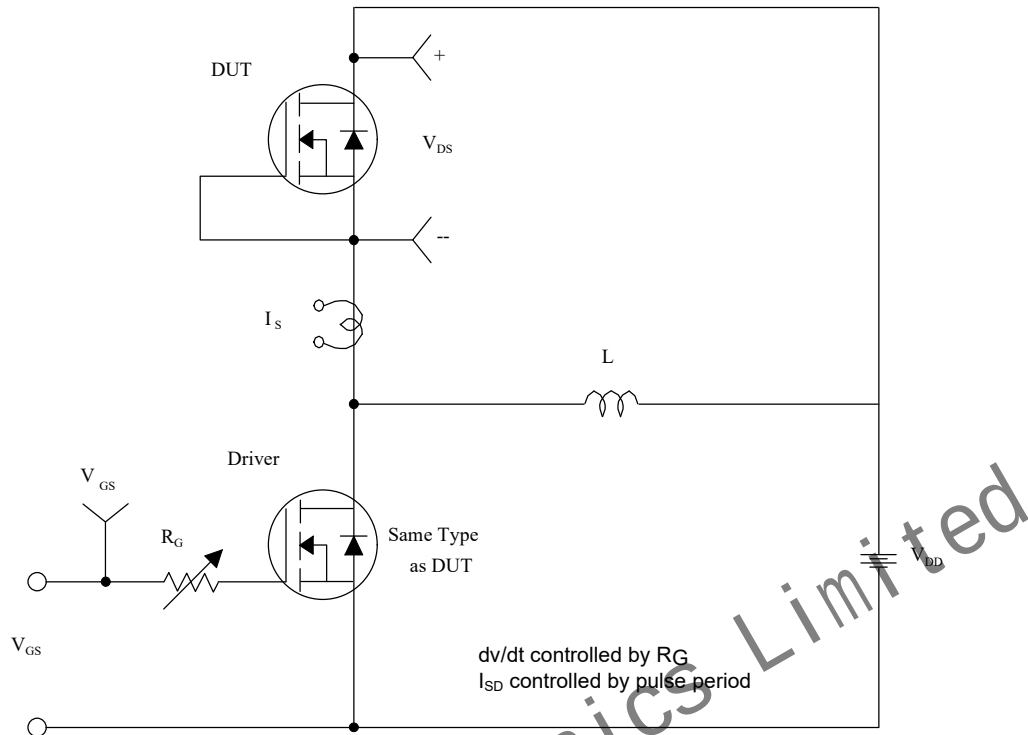
Current De-rating



Transient Thermal Impedance, Junction-Case

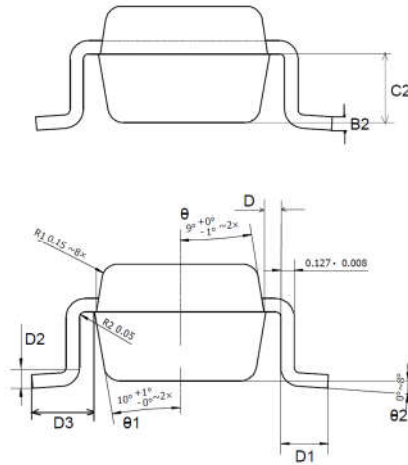
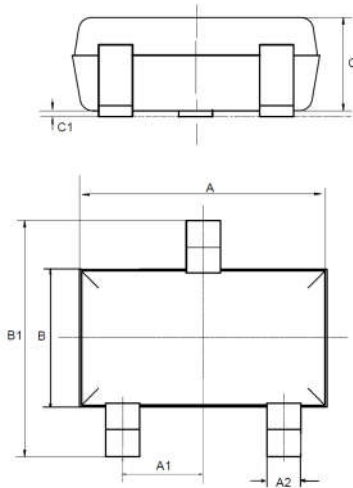
$$E_{AS} = \frac{1}{2} L_L I_{AS}^2$$

Peak Diode Recovery dv/dt Test Circuit & Waveforms



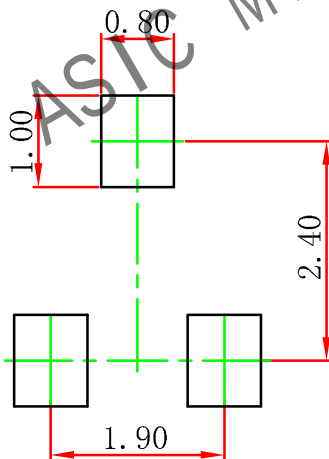
PACKAGE OUTLINE DIMENSIONS

SOT-23-3L



Symbol	Dimensions in millimeters		
	Min.	Typ.	Max.
A	2.82	2.92	3.02
A1	0.90	0.95	1.0
A2	0.35	0.4	0.45
B	1.52	1.62	1.72
B1	2.80	2.90	3.0
B2	0.12	0.128	0.135
C	1.05	1.10	1.15
C1	0.03	0.08	0.13
C2	0.6	0.65	0.7
D	0.03	0.08	0.13
D1	0.4	0.45	0.5
D2	0.25TYP		
D3	0.6	0.65	0.7

Recommended PCB Layout (Unit: mm)



Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.